



MPX Series Full Intelligent Splicing Processor Hardware User Manual M1.0





The meaning of symbols

■ Safety instructions

For your safe and correct use of equipments, we use a lot of symbols on the equipments and in the manuals, demonstrating the risk of body hurt or possible damage to property for the user or others. Indications and their meanings are as follow. Please make sure to correctly understand these instructions before reading the manual.

٨	This is A level product, which may cause radio interference in the living		
<u> </u>	environment. In this case, users may need to take the feasible measures to		
	get around the interference.		
\wedge	Remind users that the dangerous voltage without insulation occurring		
<u>/</u>	within the equipment may cause people suffer from shock.		
	CE certification means that the product has reached the directive safety		
ϵ	requirements defined by the European Union. Users can be assured about		
	the use of it.		
GI CERTIFIC	SGS certification means that the product has reached the quality		
SGS	inspection standards proposed by the world's largest SGS.		
CERTO	This product passed the ISO9001 international quality certification		
DIN SN 903 9091 Zerrifikat: 91 909039594 ISO9001:2000	(certification body: TUV Rheinland, Germany).		
Δ	Warning: in order to avoid electrical shock, do not open the machine		
DO NOT OPEN	cover, nor is the useless part allowed to be placed in the box. Please		
RISK OF ELECTRIC SHOCK	contact the qualified service personnel.		

■ General information instructions

~\\\\-	It lists the factors leading to the unsuccessful operation or set and the
A	relevant information to pay attention.



Important note



Warning

In order to ensure the reliable performance of the equipment and the safety of the user, please observe the following matters during the process of installation, use and maintenance:

The matters needing attention of installation

- ◆ Please do not use this product in the following places: the place of dust, soot and electric conductivity dust, corrosive gas, combustible gas; the place exposed to high temperature, condensation, wind and rain; the occasion of vibration and impact. Electric shock, fire, wrong operation can lead to damage and deterioration to the product, either;
- ◆ In processing the screw holes and wiring, make sure that metal scraps and wire head will not fall into the shaft of controller, as it could cause a fire, fault, or incorrect operation;
- ◆ When the installation work is over, it should be assured there is nothing on the ventilated face, including packaging items like dust paper. Otherwise this may cause a fire, fault, incorrect operation for the cooling is not free;
- ◆ Should avoid wiring and inserting cable plug in charged state, otherwise it is easy to cause the shock, or electrical damage;
- ◆ The installation and wiring should be strong and reliable, contact undesirable may lead to false action:
- ◆ For a serious interference in applications, should choose shield cable as the high frequency signal input or output cable, so as to improve the anti-jamming ability of the system.

Attention in the wiring

- ◆ Only after cutting down all external power source, can install, wiring operation begin, or it may cause electric shock or equipment damage;
- ◆ This product grounds by the grounding wires .To avoid electric shocks, grounding wires and the earth must be linked together. Before the connection of input or output terminal, please make sure this product is correctly grounded;
- ◆ Immediately remove all other things after the wiring installation. Please cover the terminals of the products cover before electrification so as to avoid cause electric shock.

Matters needing attention during operation and maintenance

- ◆ Please do not touch terminals in a current state, or it may cause a shock, incorrect operation;
- ◆ Please do cleaning and terminal tighten work after turning off the power supply. These operations can lead to electric shock in a current state;
- Please do the connection or dismantle work of the communication signal cable, the expansion module cable or control unit cable after turning off the power supply, or it may cause damage to the equipment, incorrect operation;
- Please do not dismantle the equipment, avoid damaging the internal electrical component;
- ♦ Should be sure to read the manual, fully confirm the safety, only after that can do program changes, commissioning, start and stop operation.



Matters needing attention in discarding product

- ◆ Electrolytic explosion: the burning of electrolytic capacitor on circuit boards may lead to explosion;
- ♦ Please collect and process according to the classification, do not put into life garbage;
- ◆ Please process it as industrial waste, or according to the local environmental protection regulations.



Preface



This manual mainly describes MPX -VW1616-4K, MPX -VW3232-4K, MPX -VW6868-4K intelligent splicing processor hardware introduction and hardware operation methods, main parameters, common troubleshooting solutions.

If the technical parameters and system usage in this manual are changed, the manufacturer will update the version of the manual. Please use the latest user manual.

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1. Overview

The MPX Series Hybrid Splicing Matrix is a modular LED/LCD processor designed for multimedia environments, with options in 3.5U, 6.5U, and 11U sizes. It supports 12 to 52 mixed input channels, ensuring flexibility and stability through FPGA-based hardware design. Featuring a front panel LCD display and buttons, the processor enables real-time IP address display and configuration. Its modular design supports arbitrary input/output card mixing for online maintenance and expansion, accompanied by hot-swappable fans and redundant power supplies for uninterrupted operation.

The Intelligent Splicing Processor facilitates seamless signal switching (fiber, DVI, HDMI) and supports high-resolution signal capture, splicing output, input signal cropping, and processing. With KVM management for mouse and keyboard control over multiple computers, it also supports on-screen display, banner settings, and splicing matrix integration. The system utilizes a B/S architecture for visual management through multiple clients without an external server and supports intelligent coordination for third-party device integration.

1.1. Product equipment

MPX -VW1616-4K

MPX -VW3232-4K

MPX -VW6868-4K

The hybrid splicing matrix can be formed using various input and output cards

Input cards:

MPX -VW4I-4K input card (4K×2K 4 HDMI signal input)

MPX-HDT4I-4K input card (4K×2K 4 HDBaseT signal input)

Seamless output card:

MPX-VW4O-4K (2) (4Kx2K 4 HDMI signal output)

MPX -VW2O-4K (4) (4K×2K 2 HDMI signal output)

MPX-HDT4O-4K (4Kx2K 4 HDBaseT signal output)

MPX-HDT2O-4K (4Kx2K 2 HDBaseT signal output)

Preview card:

MPX-VWP-4K preview card (video signal encoding output)

Real time monitor card:

MPX-HM-4K (4Kx2K HDMI hard echo card)

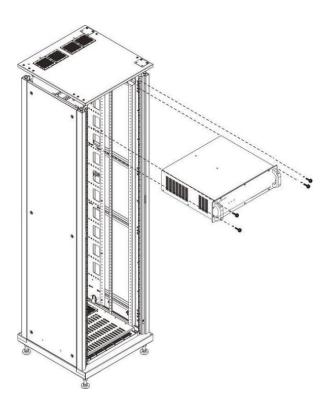


1.2. Features

- Multiple chassis size options: 3.5U, 6.5U, and 11U, supporting up to 12/32/52 channels for mixed insertion.
- Hardware audio-video switcher and splicing processor based on FPGA architecture.
- Front panel equipped with an LCD display and buttons, providing real-time display of device IP addresses and control over device switching.
- Modular design supporting mixed insertion of input/output cards, online maintenance expansion, and redundant power supply.
- Cross-conversion of various signals, including fiber, DVI, HDMI, and HDBaseT.
- Support for 4K resolution input signal capture, seamless output, and splicing output.
- A single processor supports a maximum of 120 channels of 4096 x 2160 @60Hz 4:4:4 signal input.
- Each splicing output card can achieve video splicing and functions such as image window scaling and overlay.
- Support for irregular resolution settings, with a maximum of 4096 x 2160 @60Hz 4:4:4.
- High-definition background image settings unaffected by power loss, supporting splicing base images.
- Real-time cropping, black edge removal, edge masking, and region enlargement for input videos.
- Customization of large-screen banners, welcome slogans, real-time clock display, etc.
- Parallel processing of two signals, ensuring no black screens, no flickering, no fragments, and low latency.
- KVM management, allowing mouse and keyboard control of multiple computers with remote keyboard switching.
- Support for channel logos and character overlays, with customizable character colors, sizes, and positions.
- Splicing output supports multi-layer windows, resolution of 4096 x 2160 @60Hz 4:4:4.
- Support for window locking and batch operation confirmation to ensure fixed window positions.
- Simultaneous management of multiple display wall groups, supporting real-time management and output mapping.
- Frame synchronization technology to address issues of misalignment and tearing in high-speed motion scenes.
- Intelligent scaling technology to preserve image details, eliminate jagged edges, and maintain good sharpness.



1.3. Installation





1.4. System diagram





2. Hardware description

2.1. MPX -VW1616-4K panel Diagram

MPX -VW1616-4K front panel:



MPX -VW1616-4K rear panel:





2.2. MPX -VW3232-4K panel Diagram

MPX -VW3232-4K front panel:



MPX -VW3232-4K rear panel:





2.3. MPX -VW6868-4K panel Diagram

MPX -VW6868-4K front panel:





MPX -VW6868-4K rear panel:



2.4. Connection of the matrix and peripheral devices

2.4.1 Input Interface Description

Input any combination of input signal types for MPX -VW4I-4K and MPX-HDT4I-4K

2.4.2 Output Interface Description

Output any combination of input signal types for MPX-VW4O-4K (2), MPX-VW2O-4K (4), MPX-HDT4O-4K, MPX-HDT2O-4K, and MPX-HM-4K.

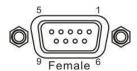


2.4.3 Control Card Ports and Connection Methods

The hybrid splicing matrix features a standard RS-232 serial communication port. It supports switching via infrared remote control and can be controlled by various systems, including personal computers, Milestone Pro control systems, and other third-party control systems

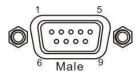
2.4.4 Matrix RS-232 Control Interface

"The hybrid splicing matrix offers 2 RS-232 serial interfaces (one DB9 female, one DB9 male) for matrix control. The pinout for the DB9 female RS-232 port is as follows:"



Pinout	Signal	Description
1	-	-
2	TXD	RS-232 protocol, send data
3	RXD	RS-232 protocol, receive data
4	-	•
5	GND	Signal Ground
6	-	1
7	-	1
8	-	•
9	-	-

The pinout for the DB9 male connector of the RS-232 port is as follows:



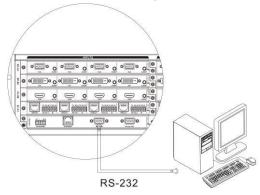
Pinout	Signal	Description
1	-	-
2	RXD	RS-232 protocol, send data
3	TXD	RS-232 protocol, receive data
4	1	-
5	GND	Signal Ground
6	-	-
7	-	-
8	-	-



9	-	-

2.4.5 Connection between the Matrix and Control Computer

Connect the computer's COM1 or COM2 serial port to the matrix host's RS-232 port using an RS-232 cable. Use control commands as outlined in 'Chapter Five, Instruction Set' for matrix control.



Matrix keyboard interface

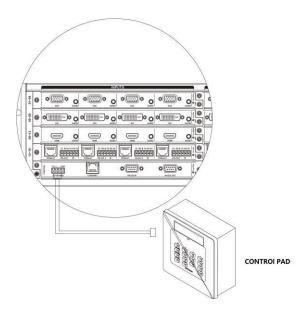
The matrix offers 1 KEYBOARD interface for connecting to the MPX-MKB100 extended keyboard, enabling channel switching. The KEYBOARD interface uses a 4-pin 3.8mm Phoenix connector with the following pinout:

Pinout	Signal	Description
1	+5V	Output DC5V/1A for MKB100
1	13 V	Provides power supply
2	+	RS-485 protocol, DATA+
3	-	RS-485 protocol, DATA-
4	GND	Signal Ground

Connection between the Matrix and Extended Keyboard

Connect the matrix host's KEYBOARD interface to the MPX-MKB100 extended keyboard's MATRIX interface using a designated cable for matrix control. Refer to the MPX-MKB100 Matrix Keyboard User Manual' for further details.





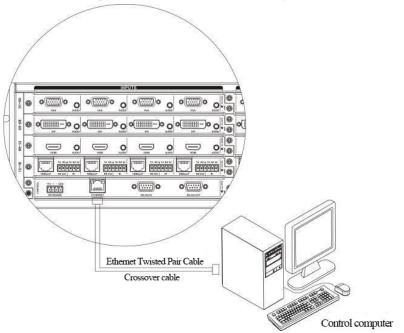
Matrix Ethernet Interface

Hardware connection method

The matrix can be hardware connected to the Ethernet adapter in two ways:

1) Cross-Connection Method

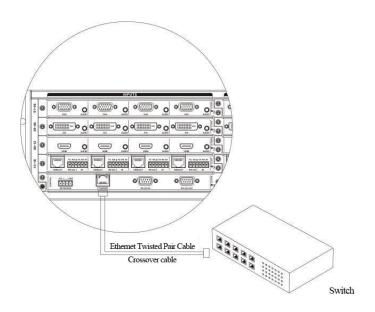
The matrix and the control computer are directly connected using a CAT5 crossover cable.



2) Straight-Through Connection Method

The matrix is connected to an Ethernet switch or hub using a straight-through CAT5 cable.

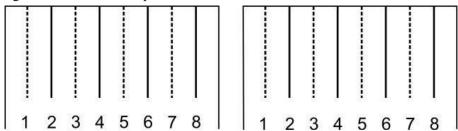




RJ45 Straight-Through and Crossover Cable Wiring Guide

Using CAT5e cables in the system, RJ-45 connectors (crystal heads) are installed at both ends to connect network devices. The standard wiring ensures symmetry for interference cancellation, with four color-coded pairs of finely twisted wires in CAT5e cables.

Two wiring standards for twisted pair cables: EIA/TIA 568B and EIA/TIA 568A.



T568A T568B

			T568A v	viring seque	nce		
1	2	3	4	5	6	7	8
White	Cassa	White	Dluc	White	Oromoso	White	D
Green	Green	Orange	Blue	Blue	Orange	Brown	Brown

			T568B wiri	ng sequence			
1	2	3	4	5	6	7	8
White	0,000,000	White	Dlue	White	Cusan	White	Danassan
Orange	Orange	Green	Blue	Blue	Green	Brown	Brown

Straight-through cable: Both ends are wired following the T568B standard.

Crossover cable: One end is wired following the T568A standard, and the other end is wired

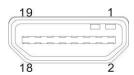


following the T568B standard.

HDMI Port Description

HDMI-A Type Line Description:

Connect various devices like DVD players, desktop computers, projectors, etc., for versatile audio-video usage, including projectors, video recorders, computer monitors, amplifiers, and more.



PI	Function			
N	1 diletion			
1	TMDS Data2+			
2	TMDS Data2 Shield			
3	TMDS Data2–			
4	TMDS Data1+			
5	TMDS Data1 Shield			
6	TMDS Data1-			
7	TMDS Data0+			
8	TMDS Data0 Shield			
9	TMDS Data0-			
10	TMDS Clock+			
11	TMDS Clock Shield			
12	TMDS Clock-			
13	CEC			
14	Reserved (in cable but N.C. on device)			
15	SCL			
16	SDA			
17	DDC/CEC Ground			
18	+5V Power			
19	Hot Plug Detect			

2



3. Hardware description

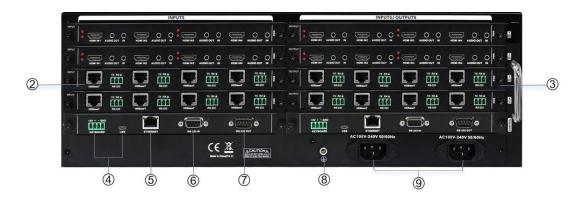
3.1. Matrix Panel Description

3.1.1 MPX -VW1616-4K Panel Description

MPX -VW1616-4K Front Panel:



MPX -VW1616-4K Rear Panel:



- A. Display Screen Shows matrix status, commands, and results.
- $B.\ 0{\sim}9\ Channel\ Buttons-Input/output\ channel\ selection,/\ and\ ALL\ for\ isolating\ multiple\ channels,$ and ALL for selecting all output\ channels.
- C. Control Command Buttons Change IP, video switch (V), matrix splice (M), save scene (SAVE), recall scene (RECALL), and menu selection.
- (1) POWER: Power, ACTIVE: Command status, SENSOR: Infrared receiver.
- 2 INPUTS Signal input ports (16/32/68).
- (3) INPUTS/OUTPUTS Signal inputs/output ports (16/32/68).
- (4) KEYBOARD Interface for MPX-MKB100 extended keyboard. USB Reserved.

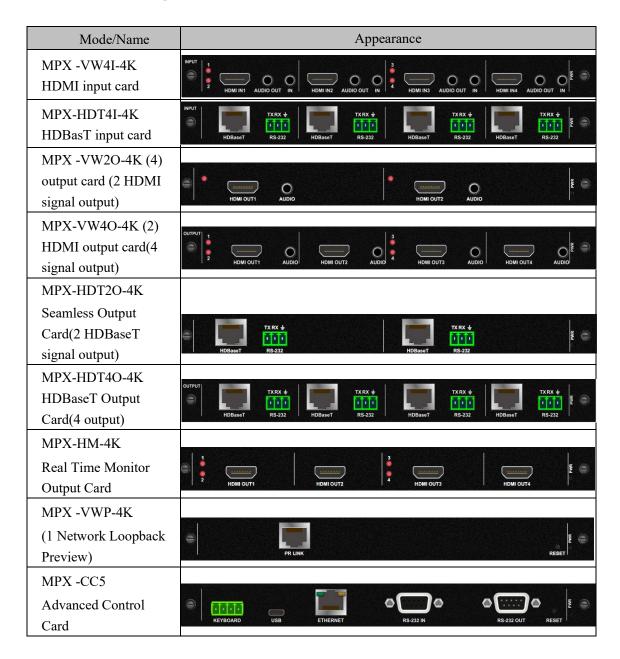


- (5) ETHERNET RJ45 network interface with indicator lights.
- 6 RS-232 IN Independent RS-232 serial input (DB9 female).
- (7) RS-232 OUT Independent RS-232 serial output (DB9 male).
- (8) Grounding Post.
- 9 Power Input AC100~240V 50/60Hz input, supports dual power redundancy.



4. Card Description

4.1. Card Categories



4.2. Input card

4.2.1 MPX -VW4I-4K input card features

◆ ★4 HDMI-A interfaces, 8 3.5mm audio jacks.



- ◆ ★Input distance up to 10 meters.
- ◆ Hot-swappable, supports seamless switching of audio and video signals.
- ◆ ★Supports selection between 3.5mm analog audio and HDMI embedded audio input.
- ◆ ★Supports digital audio de-embedding to 3.5mm audio jacks.
- ◆ Supports EDID reading function.
- ◆ Compatible with HDMI 2.0 standard, HDCP 2.2 protocol, DVI 1.0 protocol.
- ◆ Maximum supported resolution: UHDTV: 3840x2160@60Hz; DCI 4K: 4096x2160@60Hz.
- ◆ Supports fast seamless switching without flickering or black screens.
- ◆ Supports power-off scene switching memory protection and unique ESD electrostatic protection.

4.2.2 MPX-HDT4I-4K Input Card Features:

- ◆ 4 high-speed RJ45 interfaces for seamless output, with 4 3-pin Phoenix connectors.
- ◆ CAT6a cables support output distances up to 100 meters at 4K@60Hz.
- ◆ Hot-swappable design for seamless switching of audio and video signals.
- Supports serial port input/output.
- Serial port switching capabilities.
- ◆ Compatible with HDBaseT protocol.
- ◆ Maximum supported resolution: UHDTV: 3840x2160@60Hz; DCI 4K: 4096x2160@60Hz.

4.3. Output Card

4.3.1 MPX-VW4O-4K(2) Splicing Output Card Features:

- ◆ 4 HDMI-A interfaces with seamless output and 4 3.5mm audio jacks.
- ◆ Output distance up to 10 meters.
- ◆ Hot-swappable with seamless audio and video signal switching.
- ◆ ★Simultaneous output of analog and HDMI embedded audio.
- ◆ Supports EDID reading.
- ◆ HDMI 2.0, HDCP 2.2, DVI 1.0 compliant.
- ♦ Maximum resolution: UHDTV 3840x2160@60Hz; DCI 4K 4096x2160@60Hz.
- ◆ Fast seamless switching with no flickering or black screens.
- ◆ Power-off scene switching memory protection and ESD electrostatic protection.
- ◆ Single screen can open 2 windows, signals can overlay, roam, and scale freely.

4.3.2 MPX-HDT4O-4K Splicing Output Card Features:

- ◆ 4 high-speed RJ45 interfaces and 4 3-pin Phoenix connectors.
- ◆ LED/LCD splicing function.
- ◆ Uses CAT6a cables for output up to 100 meters.



- Hot-swappable and supports serial port switching.
- ◆ Compatible with HDBaseT protocol.
- ◆ Supports seamless output on 4 twisted pair channels, with onboard RS232 interface.
- ◆ Single screen can open 2 windows, signals can overlay, roam, and scale freely.

4.3.3 MPX-VW2O-4K(4) 4K Splicing Output Card Features:

- ◆ 2 HDMI-A interfaces with seamless output and 2 3.5mm audio jacks.
- ◆ Output distance up to 10 meters.
- ◆ Hot-swappable for seamless audio and video signal switching.
- ◆ ★Simultaneous output of analog and HDMI embedded audio.
- ◆ Supports EDID reading.
- ◆ HDMI 2.0, HDCP 2.2, DVI 1.0 compliant.
- ◆ ★Maximum resolution: 4K×2K@60Hz.
- ◆ Single screen can open 4 windows, signals can overlay, roam, and scale freely.

4.3.4 MPX-HDT2O-4K Splicing Output Card Features:

- ◆ 2 high-speed RJ45 interfaces and 2 3-pin Phoenix connectors.
- ◆ LED/LCD splicing function.
- ◆ Uses CAT6a cables for output up to 100 meters.
- ◆ Hot-swappable with serial port switching.
- ◆ Compatible with HDBaseT protocol.
- ◆ Supports seamless output on 2 twisted pair channels, with onboard RS232 interface.
- ◆ Single screen can open 4 windows, signals can overlay, roam, and scale freely.

4.4. Preview Card

MPX -VWP-4K Preview Card Features:

- ◆ 1 RJ45 interface for simultaneous preview of all input video signals.
- ◆ Maximum video resolution: 1080P@30fps.
- ♦ H.264 multi-stream encoding with frame rates from 1/16 to 30fps.
- ◆ Hot-swappable design.

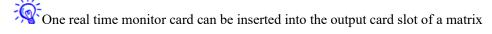
One preview card can be inserted into the output card slot of a matrix



4.5. Real Time Monitor Card

MPX-HM-4K Real Time Monitor Card Features:

- ◆ It can support up to 4 groups of splicing wall sync on display. Four groups of splicing walls can be output by one HDMI port, or divided into four HDMI ports for output. Each HDMI port displays a group of splicing wall
- ◆ Compatible with HDMI2.0 standard, HDCP2.3 protocol, DVI1.0 protocol;
- ◆ Maximum supported resolution:3840*2160@60Hz, YUV4:4:4 sampling
- ◆ With output status detection, comes with LED status indicator



4.6. Control Card

MPX -CC5 Advanced Control Card Features:

- ◆ 2 DB9 serial ports for peripheral control.
- ◆ 1 USB reserved interface.
- ◆ 1 RJ45 interface for programmable functions.
- ◆ 1 4P Phoenix keyboard interface.
- ◆ 1 3P Phoenix serial port for debugging and command reception.
- ◆ Hot-swappable design.
- ◆ Supports programmable control

4.7. Specifications and Technical Parameters

MPX -VW4I-4K & MPX-VW4O-4K (2) technical parameters

Model Specification	MPX -VW4I-4K	MPX-VW4O-4K (2)		
Protocol				
HDMI2.0 standard	HDCP2.2 protocol, DVI1.0 pro	otocol;		
Video	Video			
Gain	0dB			
Pixel Bandwidth	600MHz,full digital			
Interface	6Chma full digital/Total 19Chma anab colonia 6Chma)			
Bandwidth	6Gbps, full digital(Total 18Gbps, each color is 6Gbps)			
Resolution	4096×2160@60(Backward Compatible)			
Bit Clock Jitter	<0.15 Tbit			



		_	
Bit Rise Time	<0.3Tbit (20%80%)		
Bit Fall Time	<0.3Tbit (20%80%)		
Maximum Transfer	5nS(±1nS)		
Interface	4 HDMI-A interface 4 3.5m	m audio jack	
Signal Strength	T.M.D.S. +/- 0.4Vpp		
Minimum/Maxim um	T.M.D.S. 2.9V/3.3V		
Impedance	50 Ω		
EDID	Optional Default EDID And Reading Functionality N/A		
Maximum DC	15mV		
Suggested Maximum Input/output	Input Less than 10m, 3840x2160@ 60(Use certified HDMI cables, e.g., Molex TM cables, as recommended)	Output up to 10 meters at 3840x2160@60 resolution (Use certified HDMI cables, e.g., Molex TM cables, as recommended)	
Product Weight	About 0.5KG About 0.5KG		
Maximum Power Consumption	20W	30W	

$MPX\text{-}HDT4I\text{-}4K \ \& \ MPX\text{-}HDT4O\text{-}4K \ parameters$

Model Specification	MPX-HDT4I-4K	MPX-HDT4O-4K	
Link port Input/output			
Interfaces	4 high-speed RJ45 ports a	and 4 3-pin Phoenix connectors	
Supported protocol	HDBaseT protocol		
Pixel Bandwidth	600MHz, full digital		
Interface Bandwidth	6Gbps, full digital(Total	18Gbps, each color is 6Gbps)	
Resolution	4096×2160@60(Backward Compatible)		
Signal Type	HDBaseT high-speed differential signals defined in the protocol		
POE via Ethernet cable	Powered with POC (+12V), this card can supply power to our CAT5 series transmitters through the Ethernet cable.	Powered with POC (+12V), this card can supply power to our CAT5 series receivers through the Ethernet cable.	
Impedance	50 Ω		
EDID	Optional Default EDID	N/A	



Maximum DC	15mV		
Suggested Maximum	Up to 100 meters at 3840x2160@60 (recommended using NEXANS		
Input/output	CAT6a cables)		
Product Weight	About 0.5KG About 0.5KG		
Maximum Power Consumption	35W	45W	

MPX -VW2O-4K (4) technical parameters

Model Specification	MPX-VW2O-4K(4)	
Protocol		
HDMI2.0 standard, HD	CP2.2 protocol, DVI1.0 protocol;	
Video		
Gain	0dB	
Pixel Bandwidth	600MHz, full digital	
Interface Bandwidth	6Gbps full digital (total 18Gbps, each color is 6Gbps)	
Resolution	4096x2160@60 (Backward Compatible)	
Bit Clock Jitter (Clock Jitter)	<0.15 Tbit	
Bit Rise Time	<0.3Tbit (20%80%)	
Bit Fall Time	<0.3Tbit (20%80%)	
Maximum Transfer	5nS(±1nS)	
Interface	4 HDMI-A interface、2 3.5mm audio jack	
Signal Strength	T.M.D.S. +/- 0.4Vpp	
Minimum/Maximum	T.M.D.S. 2.9V/3.3V	
Impedance	50 Ω	
EDID	N/A	
Maximum DC	15mV	
Suggested Maximum Input/output	Output up to 10 meters at 3840x2160@60 resolution(Use certified HDMI cables, e.g., Molex TM cables, as recommended)	
Product Weight	About 0.5KGM	
Maximum Power Consumption	30W	



MPX-HDT2O-4K technical parameters:

Model	
	MPX-HDT2O-4K
Specifications	
Link port Input/output	
Interface	2 high-speed RJ45 ports and 2 3-pin Phoenix connectors
Video	
Supported protocol	HDBaseT protocol.
Pixel Bandwidth	600MHz, full digital
Interface Bandwidth	6Gbps, full digital (total 18Gbps, each color is 6Gbps)
Resolution	4096x2160@60(Backward Compatible)
Number of Windows	Single screen supports 4 windows, with overlay, roaming, and flexible zooming
Control Scale	Controls up to 68 screens, manages multiple screen groups, and supports saving/recalling 255 presets
Signal Types	High-speed differential signals defined in the HDBaseT protocol
(POE) via Ethernet cable	Powered with POC (+12V), compatible with our CAT5 series transmitters, providing power through the Ethernet cable
Impedance	50Ω
EDID	N/A
Maximum DC Bias Error	15mV
Suggested Maximum	Up to 100 meters at 3840x2160@60 (recommended using NEXANS
Input/output Distance	CAT6a dedicated cables)
Product Weigh	About 0.5KGM
Maximum Power Consumption	45W

MPX -VW1616-4K/3232/6868 Technical specifications

Model Specifications	MPX-VW1616-4K	MPX -VW3232-4K	MPX -VW6868-4K	
Protocol				
Number of				
Connectable Input	7/28	16/64	30/120	



Cards / Input Channels				
Number of Connectable Output Cards / Output Channels	4/16	8/32	17/68	
Supported Input Card Ty	pes	MPX -VW4I-4K 、 N	л ЛРХ-HDT4I-4K	
Supported Seamless Outp	`	MPX-HDT4O-4K、M	1PX-HDT2O-4K	
Supported Spliced Outpu		MPX-VW4O-4K (2)	MPX-VW4O-4K (2)、MPX -VW2O-4K (4)、	
Supported Preview Card		MPX -VWP-4K		
Support Real Time Monit		MPX-HM-4K		
Interface Bandwidth	• • • • • • • • • • • • • • • • • • • •	18Gbps		
Serial Port Control		•		
Baud Rate and Protocol		Baud Rate: 9600, Dat No Parity	ta Bits: 8, Stop Bits: 1,	
Serial Control Port Struct	Serial Control Port Structure		9-pin Female D-type Connector: 2=TX, 3=RX, 5=GND;9-pin Male D-type Connector: 2=RX, 3=TX, 5=GND	
KEYBOARD Control Int	erface			
Keyboard Control Interfa	ce	4-pin 3.8mm Phoenix Interface		
Usage Method		Used in conjunction with the MCP100 Extension Keyboard		
Keyboard Control Port St	tructure	+5V=DC5V,+=DATA	A+,-=DATA-	
Ethernet Control				
Ethernet Control Interfac	e	RJ-45 Female Connec	tor	
Ethernet Control Protoco	1	TCP/IP		
Ethernet Control Rate		Adaptive 10M/100M, Full Duplex or Half Duplex		
Specifications				
System Operating Power		100VAC~240VAC, 50/60Hz, international adaptive power supply		
Storage, Operating Temp	erature	0~+50°C		
Storage, Operating Humi	dity	20%~70%	20%~70%	
Chassis Dimensions	3.5U	6.5U	11U	
Product Weight (excluding any cards)	About 14KG	About 22KG	About 29KG	
Idle Power Consumption (excluding any cards)	About 15W	About 40W	About 90W	
Dimensions (LxWxH)mm	445x400x156	445x400x289	445x400x489	
Mean Time Between Failures	30,000h	,		



1 Warranty	l 1-vear free warranty, lifetime maintenance
vi airairty	1 year nee warrancy, meanic maintenance



5. Instruction Set

5.1. MPX-VW Matrix Commands

Serial Port Protocol: Baud Rate: 9600 Data Bits: 8 Stop Bits: 1 Parity: None Ethernet: Protocol:

TCP, IP: 192.168.1.190 Port: 6666 Symbolic Command Meanings:

[X1], [X2]... [Xn]: Corresponding input ports

[Y1], [Y2]... [Yn]: Corresponding output ports

[TX1], [TX2]... [TXn]: Serial port/infrared transmission channels for the respective input ports

[RX1], [RX2]... [RXn]: Serial port/infrared reception channels for the respective input ports

[TY1], [TY2]... [TYn]: Serial port/infrared transmission channels for the respective output ports

[RY1], [RY2]... [RYn]: Serial port/infrared reception channels for the respective output ports H represents an Arabic numeral, and n is the number of input/output interfaces for the corresponding

device model (e.g., MPX -VW1616-4K, where n can be a maximum of 16).

Remove the square brackets "[x]" when entering commands. For example, \$[x]AudioA!

should be input as \$8AudioA!

		T	Γ
Instructions (pc-	Function	Parameter	Returned messages
->X9 processor)	description	Description	Returned messages
System settings			
	Matrix model query		<vh-< td=""></vh-<>
/*Type;	(return type is	NO	VH0808>/ <vh-< td=""></vh-<>
	uncertain)		VH1616>
/^Version;	Control card version query	NO	<ver2.0.1></ver2.0.1>
<^Max_Chan>	Maximum number of channels of the device		<^Max_Chan8>/<^ Max_Chan16>
/:BellOff;	Turn off the buzzer	NO	<bell off=""></bell>
/:BellOn;	Turn on the buzzer	NO	<bell on=""></bell>
/:HeartBeat;	Heartbeat packet with device		<heartbeat></heartbeat>
/:ScanPortType;	Scan card slot	Card types include:HDMI/DVI/ VGA/SDI/CAT5/Y UV/IP/CVBS/Brow se/Fiber/HDMI_4K/ Fiber_4K/CAT_4K/ PJ_HDMI/PJ_DVI/	<port startscanning=""> <port 7="" er3.1="" hdmi="" in="" v="" ver1.2=""> <port stopscanning=""></port></port></port>



	<u> </u>	I	
		PJ_CAT/PJ_HDMI2	
		/PJ_DVI2/PJ_CAT2	
		/PJ_DMI4K/U_BO	
/.CD(D 1		OT/Unknow	<d 10<="" 14:="" 7="" i="" td=""></d>
/:ScanPortResolutio	Scan resolution		<pre><resolution 19="" 20x1080x60hz="" 7="" in=""></resolution></pre>
n;	A1 41		20X1080X60HZ>
AllTemperatureIn!	Analyze the temperature of all		<temp 24.5<="" [5,8]="" in="" td=""></temp>
An remperaturem:	input cards		>
	Analyze the		
AllTemperatureOut	temperature of all		<temp 34<="" [9,12]="" out="" td=""></temp>
!	output cards		.5>
	Analyze the status		
AllAnalyseIn!	of the input card		
	Analyze the status		
AllAnalyseOut!	of the input card		
	Query the input		
	signal		
Status[x].	corresponding to		V:[x1] -> [x2]
	the [x]th output		
	Query the input		
G	signals	Return command	
Status.	corresponding to all	same as the signal	
	outputs	switching command.	
		[x]:1 Input 2: Output	
		[y]:Channel	
		[z]:0x00: Pass-	
		through.	
		0x01: Red, green,	
		and blue color bars	
		0x02:16-level	
		grayscale.	
		0x03:32-level	
<pre><#TestColor[x],[y],</pre>	Port [y] graphic test	grayscale.	<testcolor[x],[y],[z< td=""></testcolor[x],[y],[z<>
[z],[r],[g],[b]>	mode.	0x04:64-level],[r],[g],[b]>
		grayscale.	
		0x05: Red grid	
		0x06: Green grid	
		0x07: Blue grid 0x08: White grid	
		0x08: write grid 0x09:Horizontal	
		scrollbar	
		0x0A: Vertical	
		scrollbar	
		Scronoai	



		0x0B: Solid color	
		(depending on RGB	
<pre><#SignalMode[x],[y],[z],[r],[g],[b]></pre>	No graphic input port outputs graphics (input is only solid color).	values). [x]:1 Input 2: Output [y]:Channel [z]:0x00: Pass- through. 0x01: Red, green, and blue color bars 0x02:16-level grayscale. 0x03:32-level grayscale. 0x04:64-level grayscale. 0x05: Red grid 0x06: Green grid 0x07: Blue grid 0x08: White grid 0x09:Horizontal scrollbar 0x0A:Vertical scrollbar 0x0B: Solid color (depending on RGB values).	<signalmode[x],[y], [z],[r],[g],[b]=""></signalmode[x],[y],>
<pre><#Preview192.168. 1.191></pre>	IP address of the preview card	On startup, it fetches user's previous preview video IP settings.	<preview192.168.1. 191></preview192.168.1.
Signal Switching			
[x]V[y].	Switch input [x] to output [y], video on/off.		V:[x] -> [y]
[x]V[y1],[y2],[y3].	Switch input [x] to outputs [y1], [y2], [y3], video switching		V:[x] -> [y1] V:[x] -> [y2] V:[x] -> [y3]
[x]All.	Input [x] to all outputs, close all output videos when [X1] is 0	return command same as the signal switching command	
All\$.	Close the signal of all output ports		



	Video and infrared	
All#.		
	serial ports	
	correspond directly.	
[RX1]R[TY1].	Route [RX1] to	
	[TY1] for serial	
	communication	RS:[RX1]->[TY1]
	(RS232 forward	
	channel)	
[RX1]S[TY1].	,	
	1 ' 1	
	[TY1] for serial	
	communication	TS:[RX1]->[TY1]
	(RS232 reverse	
	channel).	
[RX1]Q[TY1].	Connect input IR	
	[RX1] to output IR	
	[TY1] (Forward IR	IR:[RX1]->[TY1]
	channel switch)	
	-	
[RX1]F[TY1].	Connect output IR	
	[RX1] to input IR	TR:[RX1]->[TY1]
	[TY1] (Forward IR	
	channel switch)	
[RX1]T[TY1].	Input RS232/IR	
	[RX1] to output	
	RS232/IR [TY1]	T:[RX1]->[TY1]
	(Forward RS232/IR	5.[5.2.5]
	switch)	
	,	
[RX1]K[TY1].	KVM box	
	switching	
Matrix presets		
Corre[V]	Save the current	<save f1!="" to=""></save>
Save[Y].	state to group [Y]	<save f1!="" to=""></save>
		Return the saved
Recall[Y].	Recall the state	state of the current
	saved in group [Y].	
	Clear the state	preset.
Clear[Y].		<clear f1!=""></clear>
	saved in group [Y].	
Network settings.		
<^SPORT>	Query the current	
	matrix network port	<sport:[x1]></sport:[x1]>
	number.	
<^SIPR>	Query the current	
	matrix network IP	<sipr:[x1].[x2].[x< td=""></sipr:[x1].[x2].[x<>
	address.	3].[X4]>
	address.	



<^SUBR>	Query the current network subnet mask.	<subr:[x1].[x2].[X3].[X4]></subr:[x1].[x2].[
<^GAR>	Query the current network gateway	<gar:[x1].[x2].[x 3].[X4]></gar:[x1].[x2].[x
<^SHAR>	Query the current network hardware address.	<shar:[x1]:[x2]:[X3]:[X4]:[X5]:[X6] ></shar:[x1]:[x2]:[
<#SPORT[5000]>	Set the network port number of the matrix	<sport:[x1]></sport:[x1]>
<#SIPR[192].[168]. [0].[2]>	Set the matrix network IP address	<sipr:[x1].[x2].[x 3].[X4]></sipr:[x1].[x2].[x
<#GAR[192].[168]. [0].[1]>	Set the network gateway address.	<subr:[x1].[x2].[X3].[X4]></subr:[x1].[x2].[
<pre><#SUBR[255].[255].[255].[0]></pre>	Set the network subnet mask.	<gar:[x1].[x2].[x 3].[X4]></gar:[x1].[x2].[x
<pre><#SHAR[98]:[00]:[c1]:[00]:[00]:[01]></pre>	Set the network hardware address (in hexadecimal).	<shar:[x1]:[x2]:[X3]:[X4]:[X5]:[X6] ></shar:[x1]:[x2]:[
<#NETDEFAULT>	Restore network settings to factory defaults.	
Set preview card netw	ork parameters	
<^HSSPORT>	Query preview card port number	<hssport:[x1></hssport:[x1>
<^HSSIPR>	Query preview card IP address	<hsipr:[x1].[x2].[X3].[X4]></hsipr:[x1].[x2].[
<^HSSUBR>	Query the subnet mask number of the preview card	<hsubr:[x1].[x2] .[X3].[X4]></hsubr:[x1].[x2]
<^HSGAR>	Query the current network gateway number.	<hgar:[x1].[x2].[X3].[X4]></hgar:[x1].[x2].[
<^HSSHAR>	Query the current network hardware address for the preview card	<hshar:[x1]:[x2] :[X3]:[X4]:[X5]:[X6]></hshar:[x1]:[x2]
<#HSSPORT[5000]>	Set the preview card port number.	<hsport:[x1]></hsport:[x1]>
<pre><#HSSIPR[192].[1 68].[0].[2]> <#HSGAR[192].[1 68].[0].[1]></pre>	Set the preview card IP. Set the preview card gateway.	<pre></pre>



∠#ЦССПDDГЭ551 г	Sat the marriage		∠UСА D.[V1] [V2] [
<pre><#HSSUBR[255].[2551 [255] [0]></pre>	Set the preview card subnet mask.		<hgar:[x1].[x2].[< td=""></hgar:[x1].[x2].[<>
255].[255].[0]>			X3].[X4]>
<#HSSHAR[98]:[0	Set the preview card hardware		<hshar:[x1]:[x2]< td=""></hshar:[x1]:[x2]<>
0]:[c1]:[00]:[00]:[0			:[X3]:[X4]:[X5]:[X6
2]>	address (in]>
	hexadecimal).		
<#HSNETDEFAU	Restore factory		
LT>	settings for the		
	preview card.		ALICD 14:
			<^HSResolution_is_
			1280*720> or
			<hh><hhsresolution_is_< td=""></hhsresolution_is_<></hh>
<^HSResolution>	Current resolution.		800*600> or
			<hh><hhsresolution_is_< td=""></hhsresolution_is_<></hh>
			640*480> or
			<hh></hh> <hr/>
		Common resolutions	352*288>
∠∧HCD analu4: 12	Set the resolution of	Common resolutions	
<^HSResolution12		include: 1280*720\	
80*720>	the preview card.	800*600、640*480、	
	0 4	352*288	
ζΔII' IDS	Query the		<viewip[192].[168].< td=""></viewip[192].[168].<>
<^ViewIP>	configured preview		[0].[2]>
	stream address IP		
<#X7:ID[102] [1/	Configure the		
<pre><#ViewIP[192].[16</pre>	software to preview the video stream		
8].[0].[2]>	address IP.		
	address 1P.	0: 2*2、1: 3*3、2:	
<#ViewSplit[x]>	Set the preview	4*4	√ViovyCnlit[v]\
\# viewspin(x)	screen.	3: 5*5, 4: 6*6,	<viewsplit[x]></viewsplit[x]>
	Cat the marriage	3: 3°3°, 4: 0°0°	
<#ViewAuto>	Set the preview		<viewauto></viewauto>
	Screen.		
<^ViewSplit>	Query the preview		<viewsplit[x]></viewsplit[x]>
	Set HDML provious	0: 4*4、1: 5*5、2:	
<pre><#LoopSplit[x]></pre>	Set HDMI preview		<loopsplit[x]></loopsplit[x]>
	split screen.	6*6、	
	Device HDMI		
<#LoopAuto>	preview		<loopauto></loopauto>
	automatically split		
	Screen		
<^LoopSplit>	Query HDMI		<loopsplit[x]></loopsplit[x]>
VCA - "	preview split screen		
VGA setting			



\$[x]VGAIn!	Set to VGA signal input		<\$[x]VGAIn!>
\$[x]YUVIn!	Set to YPbPr signal input		<\$[x]YUVIn!>
<pre><#Cmd_Baud[x]></pre>	[x]: baud rate 0:9600 1:38400 2:115200 3:230400		
<pre><#Cmd_Port[x]></pre>	x: 0 COM0 1 COM1 2:KeyBoard		<cmd_port[x]></cmd_port[x]>
<^Cmd_Port>	Return command port and baud rate		<cmd_port[x]> <cmd_baud[x]></cmd_baud[x]></cmd_port[x]>
<pre><#Cmd_Send[x]></pre>	Send central control command x: command ID	Failure means there is no set command	<send_failed></send_failed>
Enter characters			
SetOSD[x].	Set input character parameters of [x]th path	Return the character status of the current port, and the meaning of the returned status is consistent with the setting command	<osd closed=""> <color255,0,0> <charpos0,0> <osd bgcolor="" closed=""> <bgcolor0,0,0> <charspeed0> <colordir0></colordir0></charspeed0></bgcolor0,0,0></osd></charpos0,0></color255,0,0></osd>
CharOpen.	Character display		<osd opened=""></osd>
CharClose.	Character display off		<osd closed=""></osd>
<pre><#CharColorR,G,B ></pre>	Character color settings		<charcolorr,g,b></charcolorr,g,b>
<pre><#CharPos[x1],[x2]></pre>	Character color settings		<charpos[x1],[x2]></charpos[x1],[x2]>
CharBgOpen.	Character background color on		<osd bgcolor<br="">Opened></osd>
CharBgClose.	Character background color off		<osd bgcolor<br="">Closed></osd>
<#CharBgColorR, G,B>	Character background color settings		<bgcolorr,g,b></bgcolorr,g,b>
<pre><#CharSpeed[x1]></pre>	Character movement speed, 0 stops moving		<charspeed[x1]></charspeed[x1]>
<pre><#CharDir[x1]></pre>	Character	1: left 2: right	<chardir[x1]></chardir[x1]>



	movement direction		
	Erase the characters		
<charerase></charerase>	of the current		<set succeed!=""></set>
	channel		
Input crop			
	Returns the		<^CropInput[x1],[x2
<^CropInput[x1]>	cropping status of],[x3],[x4],[x5]>
	input [x1]		372 372 372
	Input image crop:		
	x1: Input x2: Horizontal position		
	x3: Vertical position		
<#CropInput[x1],[x	x4: Horizontal size		<^CropInput[x1],[x2
2],[x3],[x4],[x5]>	x5: Vertical size. 0],[x3],[x4],[x5]>
	for both size		
	parameters		
	indicates no		
	cropping		
	Input color space	0: RGB	
<pre><#ColorSpace[x],[y</pre>	setting [x]: Input channel [y]:	1: YCbCr422 2: YCbCr444	ColorGrace[v] [v]
]>	channel [y]: Corresponding	3: YCbCr420	<colorspace[x],[y]></colorspace[x],[y]>
	color space	255: Auto	
	Query the color		
<^ColorSpace[x]>	space of input		<colorspace[x],[y]></colorspace[x],[y]>
	channel [x]		
Input and output setting	ıgs		
\$[x]DefaultIn!	[x]th input restores		<set succeed!=""></set>
, i i	factory settings		
ΦΕ JD C 1/O //	The [x]th output is		20 40 115
\$[x]DefaultOut!	restored to factory settings.		<set succeed!=""></set>
	Channel [x] input		
	analog		
\$[x]AudioA!	audio/Phoenix		<set succeed!=""></set>
	infrared serial port		
	input		
	Channel [x] input		
	digital		
\$[x]AudioD!	audio/network port		<set succeed!=""></set>
	infrared serial port		
	input Channel [x] output		
\$[x]AudioAOut!	analog		<set succeed!=""></set>



	audio/Phoenix infrared serial port		
\$[x]AudioDOut!	input Channel [x] output digital audio/network port infrared serial port input		<set succeed!=""></set>
\$[x]AudioOn!	Channel [x] output audio is turned on		<set succeed!=""></set>
\$[x]AudioOff!	Channel [x] output audio is turned off		<set succeed!=""></set>
All output resolutions			
<#Canvas[x],[x1],[x2],[x3]>	Set the [x]th LED resolution size x1, x2	For example, the LED resolution of port 1 is 1536x968x60Hz <#Canvas1,1536,96 8,60>	<canvas[x],[x1],[x2],[x3]></canvas[x],[x1],[x2],[x3]>
\$[x]->800x600x60 Hz!	Channel [x] output resolution 800x600x60Hz (except SDI)		<\$[x]->800x600x60 Hz!>
\$[x]->1024x768x6 0Hz!	The output resolution of channel [x] is 1024x768x60Hz (except SDI)		<\$[x]->1024x768x6 0Hz!>
\$[x]->1280x720x5 0Hz!	The output resolution of channel [x] is 1280x720x60Hz (except SDI)		<\$[x]->1280x720x5 0Hz!>
\$[x]->1280x720x6 0Hz!	The output resolution of channel [x] is 1280x720x60Hz		<\$[x]->1280x720x6 0Hz!>
\$[x]->1280x768x6 0Hz!	The output resolution of channel [x] is 1280x768x60Hz (except SDI)		<\$[x]->1280x768x6 0Hz!>



\$[x]->1280x800x6 0Hz!	The output resolution of channel [x] is 1280x800x60Hz (except SDI)	<\$[x]->1280x800x6 0Hz!>
\$[x]->1280x960x6 0Hz!	The output resolution of channel [x] is 1280x960x60Hz (except SDI)	<\$[x]->1280x960x6 0Hz!>
\$[x]->1280x1024x 60Hz!	The output resolution of channel [x] is 1280x1024x60Hz (except SDI)	<\$[x]->1280x1024x 60Hz!>
\$[x]->1360x768x6 0Hz!	The output resolution of channel [x] is 1360x768x60Hz (except SDI)	<\$[x]->1360x768x6 0Hz!>
\$[x]->1366x768x6 0Hz!	The output resolution of channel [x] is 1366x768x60Hz (except SDI)	<\$[x]->1366x768x6 0Hz!>
\$[x]->1440x900x6 0Hz!	The output resolution of channel [x] is 1400x900x60Hz (except SDI)	<\$[x]->1440x900x6 0Hz!>
\$[x]->1600x900x6 0Hz!	The output resolution of channel [x] is 1600x900x60Hz (except SDI)	<\$[x]->1600x900x6 0Hz!>
\$[x]->1600x1200x 60Hz!	The output resolution of channel [x] is 1600x1200x60Hz (except SDI)	<\$[x]->1600x1200x 60Hz!>
\$[x]->1920x1080x 25Hz!	The output resolution of channel [x] is 1920x1080x25Hz	<\$[x]->1920x1080x 25Hz!>



	(valid for SDI HDMI card)		
\$[x]->1920x1080x 30Hz!	The output resolution of channel [x] is 1920x1080x30Hz (valid for SDI HDMI card)		<\$[x]->1920x1080x 30Hz!>
\$[x]->1920x1080x 50Hz!	The output resolution of channel [x] is 1920x1080x60Hz		<\$[x]->1920x1080x 50Hz!>
\$[x]->1920x1080x 60Hz!	The output resolution of channel [x] is 1920x1080x60Hz		<\$[x]->1920x1080x 60Hz!>
\$[x]->1920x1200x 60Hz!	The output resolution of channel [x] is 1920x1200x60Hz (except SDI)		<\$[x]->1920x1200x 60Hz!>
\$[x]->1920x540x5 0Hz!	The output resolution of channel [x] is 1920x540x50Hz (1920x1080ix50Hz)	show	<\$[x]->1920x540x5 0Hz!>
\$[x]->1920x540x6 0Hz!	The output resolution of channel [x] is 1920x540x60Hz (1920x1080ix60Hz)	The sub-resolution software should show 1920x1080ix60Hz	<\$[x]->1920x540x6 0Hz!>
\$[x]->2560x1080x 25Hz!	The output resolution of channel [x] is 2560x1080x25Hz	Only 4K cards	<\$[x]->2560x1080x 25Hz!>
\$[x]->2560x1080x 30Hz!	The output resolution of channel [x] is 2560x1080x30Hz	Only 4K cards	<\$[x]->2560x1080x 30Hz!>
\$[x]->3840x2160x 25Hz!	The output resolution of channel [x] is	Only 4K cards	<\$[x]->3840x2160x 25Hz!>



	3840x2160x25Hz		
\$[x]->3840x2160x 30Hz!	The output resolution of channel [x] is 3840x2160x30Hz	Only 4K cards	<\$[x]->3840x2160x 30Hz!>
\$[x]->4096x2160x 25Hz!	The output resolution of channel [x] is 4096x2160x25Hz	Only 4K cards	<\$[x]->4096x2160x 25Hz!>
\$[x]->4096x2160x 30Hz!	The output resolution of channel [x] is 4096x2160x30Hz	Only 4K cards	<\$[x]->4096x2160x 30Hz!>
\$[x]->3840x2160x 60Hz!	The output resolution of channel [x] is 3840x2160x60Hz		<\$[x]->3840x2160x 60Hz!>
\$[x]->4096x2160x 60Hz!	The output resolution of channel [x] is 4096x2160x60Hz		<\$[x]->4096x2160x 60Hz!>
Echo settings			
Echo settings <#EchoSplit[x]>	Set echo split screen	Single screen or quad screen 0: Quad screen 1: Single screen	<echosplit[x]></echosplit[x]>
-	•	quad screen 0: Quad screen 1: Single	<echosplit[x]></echosplit[x]>
<#EchoSplit[x]>	screen Query echo split	quad screen 0: Quad screen 1: Single	
<pre><#EchoSplit[x]> <^EchoSplit> <#EchoArray[x1],[</pre>	Screen Query echo split screen Four sets of splicing walls corresponding to [x1][x4] Query the video	quad screen 0: Quad screen 1: Single	<echosplit[x]> <echoarray[x1],[x2],[x3],[x4]> <echoarray[x1],[x2< td=""></echoarray[x1],[x2<></echoarray[x1],[x2],[x3],[x4]></echosplit[x]>
<pre><#EchoSplit[x]> <^EchoSplit> <#EchoArray[x1],[x2],[x3],[x4]></pre>	Screen Query echo split screen Four sets of splicing walls corresponding to [x1][x4]	quad screen 0: Quad screen 1: Single	<echosplit[x]> <echoarray[x1],[x2],[x3],[x4]></echoarray[x1],[x2],[x3],[x4]></echosplit[x]>
<pre><#EchoSplit[x]> <^EchoSplit> <#EchoArray[x1],[x2],[x3],[x4]> <^EchoArray></pre>	Screen Query echo split screen Four sets of splicing walls corresponding to [x1][x4] Query the video wall echo settings Update echo parameters (hotswappable access	quad screen 0: Quad screen 1: Single	<echosplit[x]> <echoarray[x1],[x2],[x3],[x4]> <echoarray[x1],[x2],[x3],[x4]></echoarray[x1],[x2],[x3],[x4]></echoarray[x1],[x2],[x3],[x4]></echosplit[x]>



<^EchoState>	Query the echo		<echoopen><echo< th=""></echo<></echoopen>
EDID operation comm	status		Close>
LDID operation comin			
[D]EDIDT-[T]	Copy the EDID of		
[R]EDIDTo[T].	channel [R] output		
	to input channel [T]		
<pre><#UpdateEDID[x]/</pre>	Update the EDID of		
[data]>	the [x]th input		
UpdateEnd.	update completed		
	Copy the EDID of		
[R]EDIDTo[T].	channel [R] output		
[14]221219[1].	to input channel [T]		
ID C 1 "	to input chainer [1]		
IP Card settings		Returns the local	
CatID[w]			
SetIP[x].		network parameters	
		of the current port	
TurnLeft.	Turn left		
TurnRight.	Turn right		
TurnUp.	Turn up		
TurnDown.	Turn down		
TurnStop.	Stop turning		
FocusFar.	Focus on distance		
FocusNear.	Focus near		
<pre><#LocalSIPR[192].</pre>	Set IP card IP	<localsipr[192].[1< td=""><td></td></localsipr[192].[1<>	
[168].[0].[2]>	address	68].[0].[2]>	
<pre><#LocalGAR[192].</pre>	Set IP card IP	<pre><localgar[192].[1< pre=""></localgar[192].[1<></pre>	
[168].[0].[1]>	gateway	68].[0].[1]>	
<pre><#LocalSUBR[255</pre>	Set IP card IP	<pre></pre> <pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><pre></pre><p< td=""><td></td></p<></pre>	
].[255].[255].[0]>	subnet mask	255].[255].[0]>	
<pre><#LocalSHAR[08]:</pre>	Sautice mask	<pre></pre>	
	Set the IP card IP		
[20]:[51]:[00]:[00]:	hardware address	0]:[51]:[00]:[00]:[01	
[01]>]>	And return to the
<pre><#Connect[192].[1</pre>	Connect to current	<connect< td=""><td></td></connect<>	
68].[0].[2]>	IP card	On> <connect off=""></connect>	current split screen mode
<#SetChnStreamA	Undate the UDI		mouc
	Update the URL	<set succeed!=""></set>	
ddr/[x1]/[x2]/[x3]>	address of a specific		



<#SetChnVoMode[x]>	channel. [x1]: Channel 1 or 2, [x2]: Corresponding channel image, [x3]: Signal list ID Set split screen mode (0 to 4) single screen, 4 screens, 9 screens, 16 screens, 25 screens	<setchnvomode[x]< th=""><th></th></setchnvomode[x]<>	
<pre><#SetCardMode[x] ></pre>	Set port mode	<setcardmode[x]></setcardmode[x]>	0: Output one port image 1: Output two port images
		<ipc_chn1,0,2,2></ipc_chn1,0,2,2>	0: Not switched N: Switched ipc id
<#MULTICAST>	Set multicast address mode Set RTSP streaming mode		
/:ScanIPCList;	Get the IPC list (valid for web)		
Splicing subtitle setting	gs		
<pre>Splicing subtitle settin <^Title[x]></pre>	Returns the subtitle status of video wall [x]	Example of return code for splicing wall 2	<pre> </pre>
	Returns the subtitle status of video wall	code for splicing	<pre><^TitleLeft2> <^TitleSpeed2,0> <^TitleColor2,200,7 6,76> <^TitlePosH2,0> <^TitlePosV2,0> <^TitleBgClose2> <^TitleBgColor2,83,</pre>
<^Title[x]>	Returns the subtitle status of video wall [x] Subtitles on Subtitles off Subtitle background color	code for splicing	<pre> </pre>
<pre><^Title[x]> <#TitleOpen[x]> <#TitleClose[x]></pre>	Returns the subtitle status of video wall [x] Subtitles on Subtitles off Subtitle	code for splicing	<pre> </pre>



<pre><#TitleLeft[x]></pre>	Subtitles move left	<^TitleLeft[x]>
m Huckett[x]	Subtitles move	Thebentar
<pre><#TitleRight[x]></pre>	right	<^TitleRight[x]>
	Subtitle moving	
/#TitloSpood[v] [v]	speed (moving	<atitlespeed[v] [v]<="" td=""></atitlespeed[v]>
<pre><#TitleSpeed[x],[y] ></pre>	, ,	<pre></pre>
	speed set to 0 means	
//T'/1 C 1 [] [D]	no movement)	AT'A C L LIEN
<pre><#TitleColor[x],[R]</pre>	Subtitle color R, G,	<^TitleColor[x],[R],
,[G],[B]>	В	[G],[B]>
	Horizontal position	
<pre><#TitlePosH[x],[y]</pre>	of subtitles (used	<^TitlePosH[x],[y]>
>	when still) [y] 0 to	
	65535	
	Subtitle vertical	
<pre><#TitlePosV[x],[y]</pre>	position (multiples	<^TitlePosV[x],[y]>
>	of 16 pixels) [y]	\ \text{Title1 03 V [\Lambda],[\gamma]}
	max 255	
	Videowall[x]	
<pre><#TitleUpdate[x]></pre>	Subtitle status	<set succeed=""></set>
	undate	
<pre><#ErasePic[x]></pre>	Erase videowall[x]	<erasepic[x]></erasepic[x]>
	basemap	
<pre><#EraseTitle[x]></pre>	Erase videowall[x]	<erasetitle[x]></erasetitle[x]>
	subtitles Basemap status	
<^Pic_Sta>	query	
WDIG GLOGEL I	Videowall[x]	PIC CLOSEL I
<pre><#PIC_CLOSE[x]></pre>	basemap off	<pic_close[x]></pic_close[x]>
∠#DIC ODEN[v]>	Splicing wall [x]	CDIC ODENIA
<pre><#PIC_OPEN[x]></pre>	base image is	<pic_open[x]></pic_open[x]>
<#PIC Snap[x]>	Channel[x]	<pic snap[x]=""></pic>
	basemap screenshot	222_237[[4]
<pre><#SavePic[x]></pre>	Save the splicing	<savepic[x]></savepic[x]>
	wall [x] base image	
	Real-time time	
	display switch [x]:	
<pre><#CLOCK_SWIT</pre>	Port [y]: 11 Turn on	
CH[x],[y]>	time and date 1:	
	Turn on time 10:	
	Turn on date	
<#CLOCK SIZE[x	Real time display	
],[y]>	size (maximum	
3/10/3	255)	
<pre><#CLOCK POS[x]</pre>	Real-time time	
,[y],[z]>	display position [y]:	
\(\frac{1}{2}\) \(\frac{1}{2}\)	horizontal position	



	[z]: vertical position		
<pre><#Rtc_Clock[x],[x 1],[x2],[x3],[x4],[x 5]></pre>	Set system time year, month, day, hour, minute, second, week	For example, Tuesday, November 16, 2021 14:30:05 <#Rtc_Clock2021/1 1/16/14/30/05/2>	
<^CLOCK_INFO[x]>	Clock display status of port [x]	Return switch, size and position	
Stitching settings		-	
<#SetDelay[x],[y]>	Set the delay of splicing port [x], [y] delay size		<setdelay[x],[y]></setdelay[x],[y]>
<#MARGINH[x],[x1]>	Splicing wall [x] Horizontal screen spacing [x1]: Splicing screen spacing		<marginh[x],[x1]></marginh[x],[x1]>
<#MARGINV[x],[x1]>	Splicing wall [x] Vertical screen spacing [x1]: Splicing screen spacing		<marginv[x],[x1]></marginv[x],[x1]>
<#MAP[x],[x1],[x2]	Videowall [x] window [x1] mapped to [x2]		<map[x],[x1],[x2]></map[x],[x1],[x2]>
<#SIZE[x],[x1],[x2]	Video wall [x] resolution [x1], [x2]>	Unified settings for LCD	<size[x],[x1],[x2]></size[x],[x1],[x2]>
<pre><#SIZE_X[x],[x1], [x2],[x3],[x4]></pre>	Video wall [x] Horizontal resolution of each window	[x1][x4] corresponds to the first to fourth columns	<size_x[x],[x1],[x 2],[x3],[x4]></size_x[x],[x1],[x
<#SIZE_Y[x],[x1], [x2],[x3]>	Videowall [x] Vertical resolution of each window	[x1][x3] corresponds to the first to third columns	<size_y[x],[x1],[x 2],[x3]></size_y[x],[x1],[x
<#LOGICVIR[x],[x1],[x2]>	Splicing wall [x] rows and columns [x1]: number of horizontal window rows [x2]: number of vertical window rows	Virtually re-dividing video wall rows	<logicvir[x],[x1],[x2]></logicvir[x],[x1],[x2]>
<#VIR[x],[x1],[x2]	Splicing wall [x]		<vir[x],[x1],[x2]></vir[x],[x1],[x2]>



> <#OPEN[x],[x1],[x 2],[x3],[x4],[x5],[x 6],[x7]>	rows and columns [x1]: number of horizontal window rows [x2]: number of vertical window rows Splicing wall [x] rows and columns [x1]: number of horizontal window rows [x2]: number	<open[x],[x1],[x2] ,[x3],[x4],[x5],[x6],[="" x7]=""></open[x],[x1],[x2]>
	of vertical window rows Moving window parameter settings:	
<#MOVE[x],[x1],[x2],[x3]>	[x]: Splicing wall identification [x1]: Window identification [x2]: Window horizontal position [x3]: Window vertical position	<move[x],[x1],[x2],[x3]></move[x],[x1],[x2],[x3]>
<pre><#RESIZE[x],[x1], [x2],[x3],[x4],[x5]></pre>	Window stretching parameter settings: [x]: Splicing wall logo [x1]: Window logo [x2]: Window horizontal position [x3]: Window vertical position [x4]: Window horizontal size [x5]: Window vertical size	<resize[x],[x1],[x 2],[x3],[x4],[x5]></resize[x],[x1],[x
<#LAYER[x],[x1],[x2]>	Window layer parameter settings: [x]: Splicing wall identification [x1]: Window identification [x2]: Layer number	<layer[x],[x1],[x 2]></layer[x],[x1],[x
<#CLOSE[x],[x1]>	Window closing settings: [x]:	<close[x],[x1]></close[x],[x1]>



	Splicing wall logo [x1]: Window logo		
<pre><#CLOSEALL[x]></pre>	Close all windows of videowall [x]		<closeall[x]></closeall[x]>
<^JOINT>	Query the splicing status of all splicing walls	The return code is the same as the window return code	
<^JOINT[x]>	Query all splicing status of [x] splicing wall	The return code is the same as the window return code	
<^SIZE>	Query PC window size	The return code LCD prefix is SIZE LED is " <size_x <size_y"<="" td=""><td><size[x],[x1],[x2]></size[x],[x1],[x2]></td></size_x>	<size[x],[x1],[x2]></size[x],[x1],[x2]>
<^VIR>	Query the PC window array		<vir[x],[x1],[x2]></vir[x],[x1],[x2]>
<^LOGICVIR>	Query the logical virtual screen queue		<logicvir[x],[x1],[x2]></logicvir[x],[x1],[x2]>
<^MAP>	Query mapping relationship		<map[x],[x1],[x2]></map[x],[x1],[x2]>
<^MARGIN>	Query the setting parameters of screen spacing		<margin[x],[x1]></margin[x],[x1]>
<pre><#SavePJ[x],[Y]></pre>	Save the current mode of video wall [x] to group [y]		<savepj[x],[y]></savepj[x],[y]>
<pre><#RecallPJ[x],[Y]></pre>	Call the saved plan of group [y] of splicing wall [x]	The return code is the same as the window return code	
<#ClearPJ[x],[Y]>	Clear the plan saved in group [y] of splicing wall [x]		<clearpj[x],[y]></clearpj[x],[y]>
[x]M[x1],[x2]	[x]: Input source [x1]: Splicing wall [x2]: Splicing window	Replace the input source of window [x2]	[x]M[x1],[x2]

6. Operation instructions

6.1. Seamless output card switching

Example 1. Switch the matrix "first input" to "second output", then the key operation is:



Press in sequence	Display printing	Remark
1	1	Select "First Input"
37/M	1V	Press once to switch the matrix
V/M	l V	V
2	1V2	Select "Second Output"
		Switching completed. If there
ENTER	SwitchOK!	is no input connected to the
		card, it will display Notonline!

Example 2. To switch the matrix "first input" to "second output", and "third output", the key operations are:

Press in sequence	Display printing	Remark
1	1	Select "First Input"
V/M	1V	Press once to switch the matrix
V/IVI	1 V	V
2	1V2	Select "Second Output"
,	1V2,	delimiter
3	1V2,3	Select 'Third Output
		Switching completed. If there
ENTER	SwitchOK!	is no input connected to the
		card, it will display Notonline!

Example 3. Switch the matrix "first input" to "all outputs", then the key operation is:

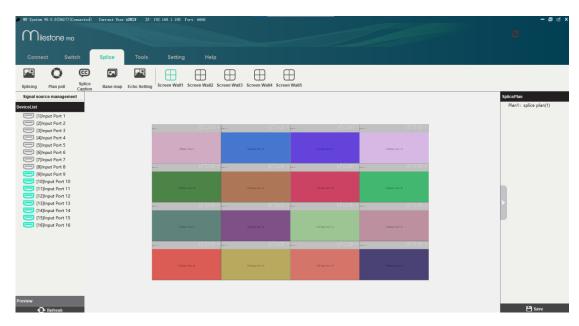
Press in sequence	Display printing	Remark
1	1	Select "First Input"
ALL	1All.	Switch completed

Note: The above example operation is limited to seamless output card switching.

6.2. Splicing output card switching

Pressing the "V/M" key toggles between seamless ("V") and splicing ("M") output card switching. There are five windows with IDs 1 to 5 on "ScreenWall1."





To display the signal from "Input 1" in "Window ID:5" on "Wall1," press the corresponding key sequence.

Press in sequence	Display printing	Remark
1	1	Select "First Input"
V/M	1V	One press for matrix switching
V/M	1M	One more press for splicing
		switching.
1	1M1	Select 'Splicing Wall 1
/	1M1/5	Interval
5	1M1/5	Select 'Window ID: 5'
ENTER	SwitchOK!	Switching completed

Note: This operation is limited to switching the splicing output card.

6.3. Save call switching state

To save the current seamless output card switching state and assign it to a number (0-9) for future recall, the key operation is:

Press in sequence	Display printing	Remark
SAVE	SaveSwitchPlan	
2	SaveSwitchSwitchOK!	Successfully saved to 'State 2'

To recall a previously saved seamless output card switching state in the matrix, the key operation is:

Press in sequence	Display printing	Remark
RECALL	RecallSwitchPlan	
2	RecallSwitchSwitchOK!	Successfully saved to 'State 2'

To save the current splicing output card switching state in the matrix, the key operation is:

		-
Press in sequence	Display printing	Remark
1	1 1 1 1 8	



SAVE	SaveSwitchPlan	Pressing once saves the
SAVE		seamless output card state
SAVE	SavePJ	Pressing again saves the
SAVE	Wall:Plan:	splicing output card state
1	SavePJ	Calast Culisin a Wall 11
1	Wall:1Plan:	Select 'Splicing Wall 1'
1	SwitchOK!	Choose to save in 'Plan1'
	Wall:1Plan:1	Choose to save in Plan1

To recall a previously saved splicing output card switching state in the matrix, the key operation is:

Press in sequence	Display printing	Remark
RECALL	RecallSwitchPlan	Pressing once saves the
RECALL	Recanswitchi ian	seamless output card state
DECALL	RecallPJ	Pressing again saves the
RECALL	Wall:Plan:	splicing output card state
1	RecallPJ	Calact Calicin a Wall 1!
	Wall:1Plan:	Select 'Splicing Wall 1'
1	SwitchOK!	Cl 4 : - : D1 1!
1	Wall:1Plan:1	Choose to save in 'Plan1'

6.4. Query settings

Query network parameters.

Press in sequence	Display printing	Remark
10	IPSETTING	Charry ID address
IP	192.168.001.190	Show IP address
ID	PORTSETTING	Charry mont myssilian
IP	6666	Show port number
ID	GATEWAYSET	Cl
IP	192.168.001.001	Show gateway
IP	SubnetMaskSET	Cll 4l-
	255.255.255.000	Show subnet mask

To modify the network parameters and change the IP address from the original 192.168.001.190 to 192.168.001.180, the key operation is:

Press in sequence	Display printing	Remark
ΙP	IPSETTING	Show IP address
11	192.168.001.190	Show if address
•	IP SETTINGS 192.168.001.190	Enter edit mode, now the edit cursor is displayed

5



d or ▶	IP SETTINGS 192.168.001.190	Move the cursor to the desired modification position
8	IP SETTINGS 192.168.001.180	Input the desired modification value
ENTER	Set Succeed!	Set Succeed!

To change the port number, gateway, and subnet mask, press the "IP" key to enter the interface, and then press

for modification.

Note: Network parameter changes require a restart.

Note: Use the web interface for modifying network parameters; key operations won't apply.



7. Common troubleshooting methods

Malfunction phenomenon	Solutions	
Matrix unable to switch	• Check for correct command input. A single beep indicates the device received the correct command and processed it	
	• If no response from the device, check wiring. The 'active' indicator on the front panel should blink when data is received.	
	• Check if the device is receiving proper power supply.	
Matrix output has no image	• Check device ports by sending a scan slot command or using PC software. Functional cards display type (HDMI/DVI, etc.) and firmware version.	
	• Check for input signals using scan slot command or PC software. 'No input' will be displayed when there's no signal.	
	• No input source, corresponding lights on matrix output won't light up. Check if the display end detects the signal.	
	• If no signal is detected, check if the display device works by direct connection. Ensure the display supports the matrix's resolution. If not, change the matrix resolution. Test the matrix directly with a monitor	
	• Check if cables are in good condition. Test by using a different video cable	
Matrix input has no image	• Verify input signals using scan slot command or PC software. 'No input' indicates no signal	
	• If 'no input' is shown, check the monitor for incoming signals. When there's input, the light next to the input port turns red.	
	• Please confirm if the resolution of the input device is supported by our matrix (refer to the manual for details).	
	 Check cables, test with a different video cable Refer to 'No Image on Matrix Output' 	
Stitching output has no image	• Check if splicing parameters are correctly set. Follow the sequence of combination, screen mapping, and resolution. Ensure correct mapping of windows to output ports. Refresh the software to confirm.	



I	• Confirm and deciding the state of the stat	
	• Confirm valid signal for the drawn window's input	
	source.	
	• Switch different inputs to a fixed output to identify	
	input or output end issues.	
	Confirm if the interfaces are properly connected.	
Video flickers or jitters	• Verify if the cables are in good condition. Test by	
J	replacing the cables.	
	• Lower resolution for testing. If low resolution is fine	
	but high resolution has issues, replace with a good or	
	shorter cable	
	• Switch different input signals to different output	
	devices to confirm if it's an input or output issue	
	• If output is not full screen, adjust display ratio on the	
	output device. For VGA, use auto-adjust or adjust matrix	
	resolution.	
	• If input is not full screen, adjust input resolution or test	
	directly with the display device to confirm signal source	
	is normal.	
The signal is not full		
	• If there's a black border on the input end, update the	
	matrix's input card EDID:	
	• Connect a display device that fills the screen.	
	• Connect it to the matrix's HDMI or DVI output.	
	• Read EDID from output to input or use software's	
	EDID function.	
	• EDID function is only for HDMI or DVI.	
	·	
	• Check cables, replace or use a shorter one for testing.	
	• Confirm if the display device is functioning properly.	
Image color is abnormal	• If VGA is not working, send commands or use PC	
	software to adjust VGA color parameters	
	• Check if the input device audio is normal. When	
	connecting a computer input, ensure the computer's audio	
	is set to digital audio output, which can be checked in the	
	computer's audio properties.	
	companies and properties.	
Output audio is abnormal	• Whether the output device supports audio input.	
	• When there is no audio on the HDMI input card, please	
	confirm whether the HDMI input is analog or digital. The	
	input can only be analog or digital, and the output supports	
	simultaneous analog and digital input out.	

If you need other details, please contact the manufacturer, thank you!